



SDRAM PART NUMBERING

- Ichon Site Product

HY	XX	X	XXX	XX	X	X	X	XX	XX	-	XX	
											SPEED*	
											5	: 5ns(200MHz)
											55	: 5.5ns(183MHz)
											6	: 6ns(166MHz)
											7	: 7ns(143MHz)
											75	: 7.5ns (133MHz)
											8	: 8ns (125MHz)
											10P	: 10ns (100MHz) CL2&3
											10S	: 10ns (100MHz) CL3
											10	: 10ns (100MHz)
											12	: 12ns (83MHz)
											15	: 15ns (66MHz)
											PACKAGE	
											TC	: 400mil TSOPII
											TQ	: 100Pin- TQFP
											POWER CONSUMPTION	
											BLANK	: Normal Power
											L	: Low Power
											DIE GENERATION	
											BLANK	: 1st Generation
											A	: 2nd Generation
											B	: 3rd Generation
											C	: 4th Generation
											D	: 5th Generation
											INTERFACE	
											0	: LVTTL
											1	: SSTL
											2	: SSTL2

HYUNDAI
Memory Product

PRODUCT GROUP
57 : SDRAM
5D : DDR SDRAM

PROCESS & POWER SUPPLY
V : CMOS, 3.3V
U : CMOS, 2.5V

DENSITY & REFRESH
4 : 4M bits, 1K Ref.
16 : 16M bits, 4K Ref.
64 : 64M bits, 8K Ref.
65 : 64M bits, 4K Ref.
129 : 128M bits, 4K Ref.
257 : 256M bits, 8K Ref.

DATA WIDTH
40 : x4
80 : x8
16 : x16
32 : x32

BANK
1 : 2Bank
2 : 4Bank

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POWER CONSUMPTION
BLANK : Normal Power
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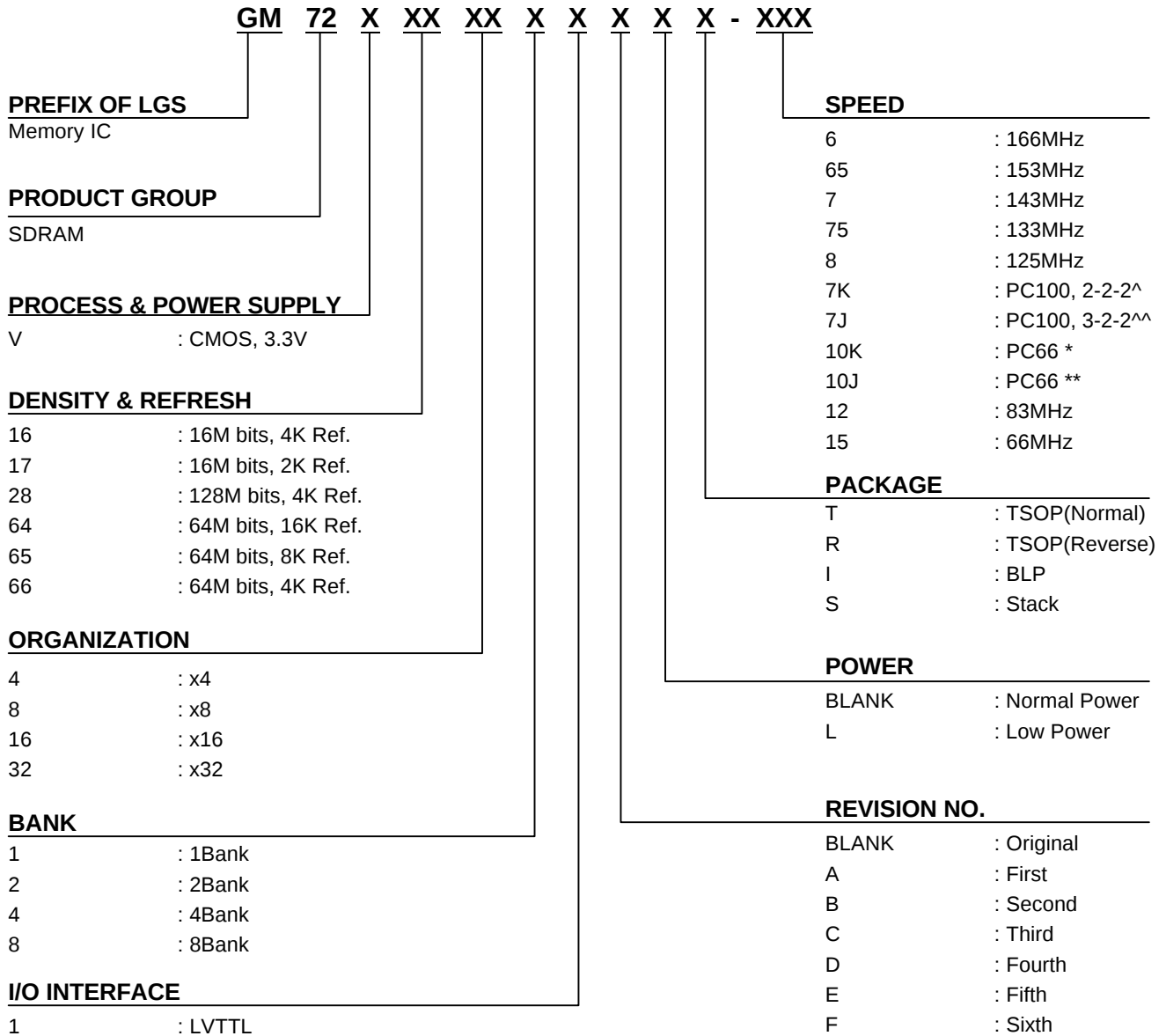
DIE GENERATION
BLANK : 1st Generation
A : 2nd Generation
B : 3rd Generation
C : 4th Generation
D : 5th Generation

INTERFACE
0 : LVTTL
1 : SSTL
2 : SSTL2

* Note : 10P / 10S - PC100 Compliant
10 : PC66 Compliant

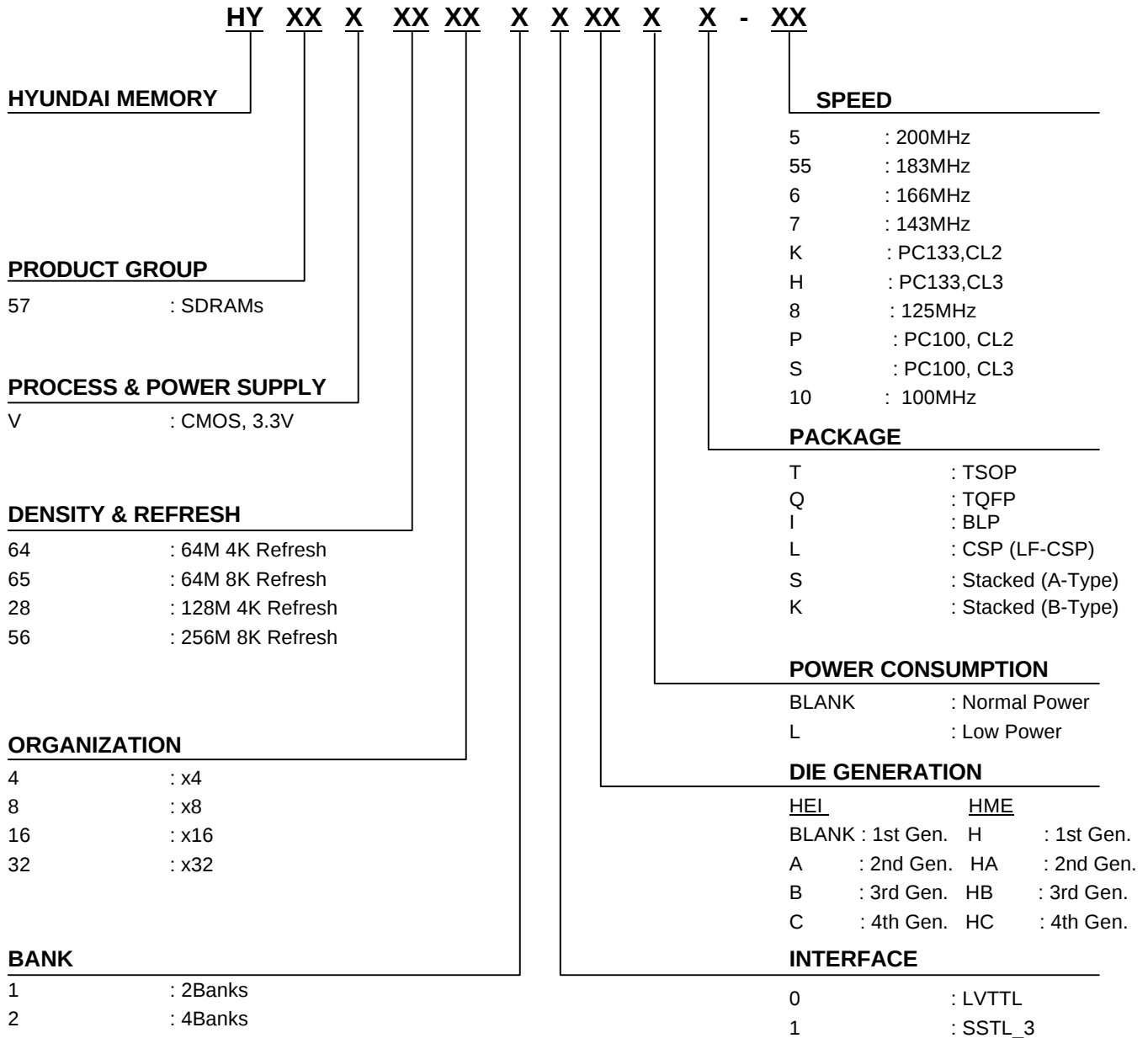
SDRAM PART NUMBERING

- Cheong-ju Site Product



Note) ^ 7K means to meet tCK=10ns, CL=2, tAC=6ns
 ^^ 7J means to meet tCK=10ns, CL=3, tAC=6ns
 * 10K means to meet tCK=15ns, CL=2, tAC=9ns
 ** 10J means to meet tCK=10ns, CL=3, tAC=9.5ns

New SDRAM PART NUMBERING



Applicable Part List (Examples of x4 Model)

64M	G	HY57V64420HGT	(Cheong-ju)
128M	3rd	HY57V28420AT	(Ichon)
	C	HY57V28420HCT	(Cheong-ju)
256M	1st	HY57V56420T	(Ichon)
	2nd	HY57V56420AT	(Ichon)

*To the newly integrated part numbering system, only x4/x8/x16 configurations are applicable