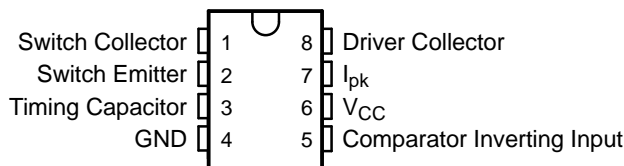


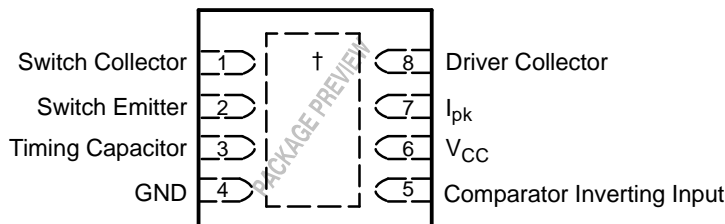
## FEATURES

- Wide Input Voltage Range...3 V to 40 V
- High Output Switch Current...Up to 1.5 A
- Adjustable Output Voltage
- Oscillator Frequency...Up to 100 kHz
- Precision Internal Reference...2%
- Short-Circuit Current Limiting
- Low Standby Current

**D (SOIC) OR P (PDIP) PACKAGE**  
(TOP VIEW)



**DRJ (QFN) PACKAGE**  
(TOP VIEW)



† Exposed thermal pad is connected internally to GND via die attach.

## DESCRIPTION/ORDERING INFORMATION

The MC33063A and MC34063A are easy-to-use ICs containing all the primary circuitry needed for building simple dc-dc converters. These devices primarily consist of an internal temperature-compensated reference, a comparator, an oscillator, a PWM controller with active current limiting, a driver, and a high-current output switch. Thus, the devices require minimal external components to build converters in the boost, buck, and inverting topologies.

The MC33063A is characterized for operation from  $-40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ , while the MC34063A is characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

## ORDERING INFORMATION

$T_A$	PACKAGE <sup>(1)</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING
$-40^{\circ}\text{C}$ to $85^{\circ}\text{C}$	PDIP – P	Tube of 50	MC33063AP	MC33063AP
	QFN – DRJ	Reel of 1000	MC33063ADRJR	PREVIEW
	SOIC – D	Tube of 75	MC33063AD	M33063A
		Reel of 2500	MC33063ADR	
$0^{\circ}\text{C}$ to $70^{\circ}\text{C}$	PDIP – P	Tube of 50	MC34063AP	MC34063AP
	QFN – DRJ	Reel of 1000	MC34063ADRJR	PREVIEW
	SOIC – D	Tube of 75	MC34063AD	M34063A
		Reel of 2500	MC34063ADR	

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



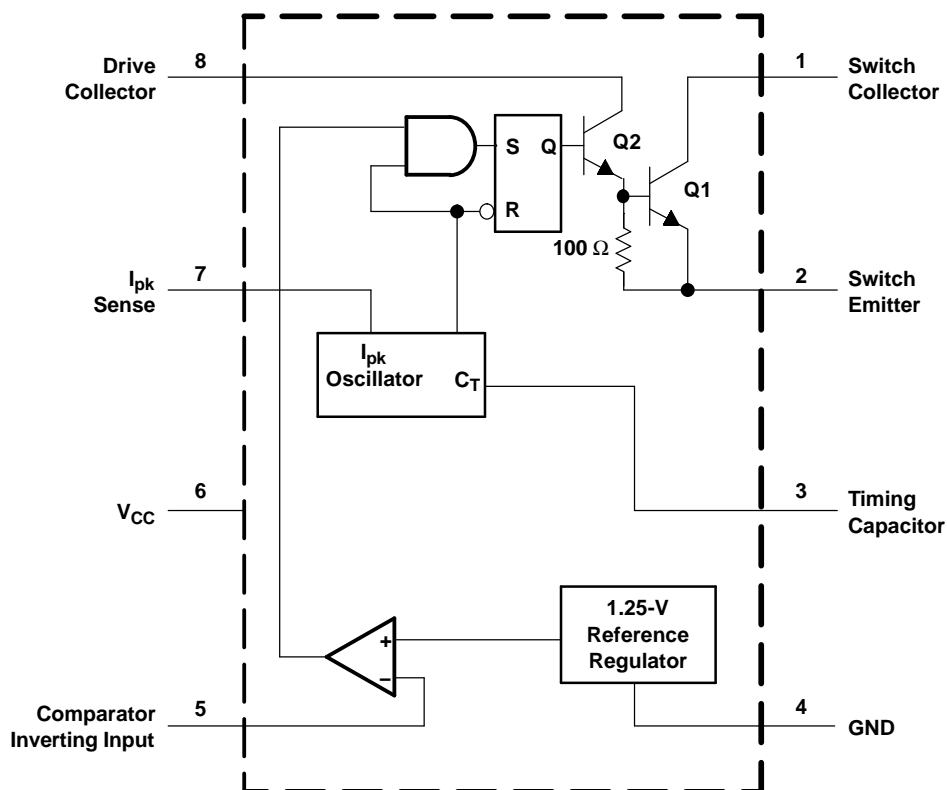
Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

# MC33063A, MC34063A

## 1.5-A PEAK BOOST/BUCK/INVERTING SWITCHING REGULATORS

SLLS636J–DECEMBER 2004–REVISED OCTOBER 2005

**FUNCTIONAL BLOCK DIAGRAM**



### Absolute Maximum Ratings<sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT
$V_{CC}$	Supply voltage		40	V
$V_{IR}$	Comparator Inverting Input voltage range	-0.3	40	V
$V_{C(switch)}$	Switch Collector voltage		40	V
$V_{E(switch)}$	Switch Emitter voltage		40	V
$V_{CE(switch)}$	Switch Collector to Switch Emitter voltage		40	V
$V_{C(driver)}$	Driver Collector voltage		40	V
$I_{C(driver)}$	Driver Collector current		100	mA
$I_{SW}$	Switch current		1.5	A
$\theta_{JA}$	Package thermal impedance <sup>(2)(3)</sup>			°C/W
	D package		97	
	DRJ package		TBD	
	P package		85	
$T_J$	Operating virtual junction temperature		150	°C
$T_{stg}$	Storage temperature range	-65	150	°C

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.
- (3) The package thermal impedance is calculated in accordance with JESD 51-7.

## Recommended Operating Conditions

		MIN	MAX	UNIT
$V_{CC}$	Supply voltage	3	40	V
$T_A$	Operating free-air temperature	MC33063A	–40	85
		MC34063A	0	70
				°C

## Electrical Characteristics

$V_{CC} = 5\text{ V}$ ,  $T_A = \text{full operating range}$  (unless otherwise noted) (see block diagram)

### Oscillator

PARAMETER	TEST CONDITIONS	$T_A$	MIN	TYP	MAX	UNIT
$f_{osc}$	Oscillator frequency	$V_{PIN5} = 0\text{ V}$ , $C_T = 1\text{ nF}$	25°C	24	33	42
$I_{chg}$	Charge current	$V_{CC} = 5\text{ V to }40\text{ V}$	25°C	24	35	42
$I_{dischg}$	Discharge current	$V_{CC} = 5\text{ V to }40\text{ V}$	25°C	140	220	260
$I_{dischg}/I_{chg}$	Discharge-to-charge current ratio	$V_{PIN7} = V_{CC}$	25°C	5.2	6.5	7.5
$V_{IpK}$	Current-limit sense voltage	$I_{dischg} = I_{chg}$	25°C	250	300	350
						mV

### Output Switch<sup>(1)</sup>

PARAMETER	TEST CONDITIONS	$T_A$	MIN	TYP	MAX	UNIT
$V_{CE(sat)}$	Saturation voltage – Darlington connection	$I_{SW} = 1\text{ A}$ , pins 1 and 8 connected	Full range	1	1.3	V
$V_{CE(sat)}$	Saturation voltage – non-Darlington connection <sup>(2)</sup>	$I_{SW} = 1\text{ A}$ , $R_{PIN8} = 82\ \Omega$ to $V_{CC}$ , forced $\beta \sim 20$	Full range	0.45	0.7	V
$h_{FE}$	DC current gain	$I_{SW} = 1\text{ A}$ , $V_{CE} = 5\text{ V}$	25°C	50	75	
$I_{C(off)}$	Collector off-state current	$V_{CE} = 40\text{ V}$	Full range	0.01	100	$\mu\text{A}$

- (1) Low duty-cycle pulse testing is used to maintain junction temperature as close to ambient temperature as possible.  
 (2) In the non-Darlington configuration, if the output switch is driven into hard saturation at low switch currents ( $\leq 300\text{ mA}$ ) and high driver currents ( $\geq 30\text{ mA}$ ), it may take up to  $2\ \mu\text{s}$  for the switch to come out of saturation. This condition effectively shortens the off time at frequencies  $\geq 30\text{ kHz}$ , becoming magnified as temperature increases. The following output drive condition is recommended in the non-Darlington configuration:  
 Forced  $\beta$  of output switch =  $I_{C,SW} / (I_{C,driver} - 7\text{ mA}) \geq 10$ , where  $\sim 7\text{ mA}$  is required by the  $100\text{-}\Omega$  resistor in the emitter of the driver to forward bias the  $V_{be}$  of the switch.

### Comparator

PARAMETER	TEST CONDITIONS	$T_A$	MIN	TYP	MAX	UNIT
$V_{th}$	Threshold voltage	25°C	1.225	1.25	1.275	V
		Full range	1.21		1.29	
$\Delta V_{th}$	Threshold-voltage line regulation	$V_{CC} = 5\text{ V to }40\text{ V}$	Full range	1.4	5	mV
$I_{IB}$	Input bias current	$V_{IN} = 0\text{ V}$	Full range	–20	–400	nA

### Total Device

PARAMETER	TEST CONDITIONS	$T_A$	MIN	MAX	UNIT
$I_{CC}$	Supply current	$V_{CC} = 5\text{ V to }40\text{ V}$ , $C_T = 1\text{ nF}$ , $V_{PIN7} = V_{CC}$ , $V_{PIN5} > V_{th}$ , $V_{PIN2} = \text{GND}$ , All other pins open	Full range	4	mA

## TYPICAL CHARACTERISTICS

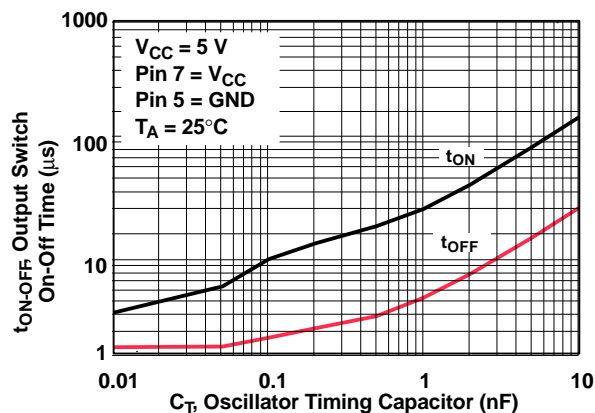


Figure 1. Output Switch On-Off Time vs Oscillator Timing Capacitor

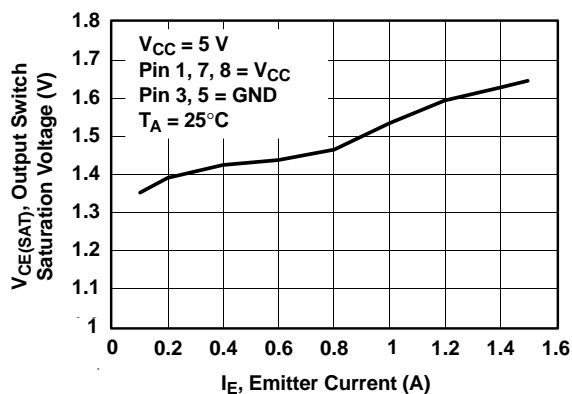


Figure 2. Output Switch Saturation Voltage vs Emitter Current (Emitter-Follower Configuration)

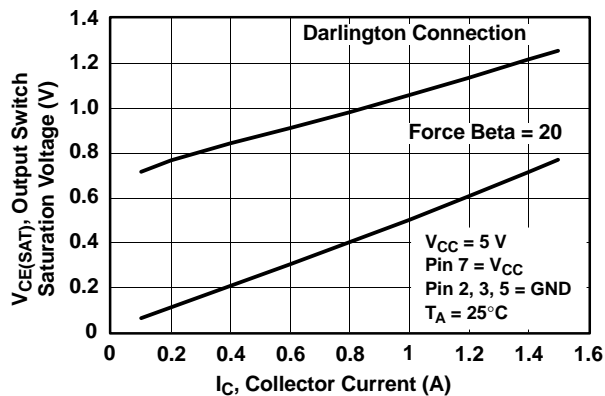


Figure 3. Output Switch Saturation Voltage vs Collector Current (Common-Emitter Configuration)

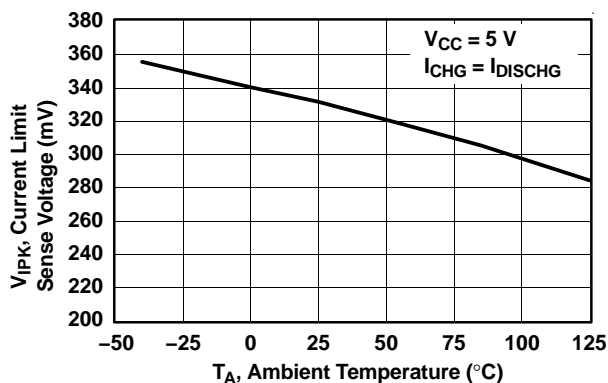


Figure 4. Current-Limit Sense Voltage vs Temperature

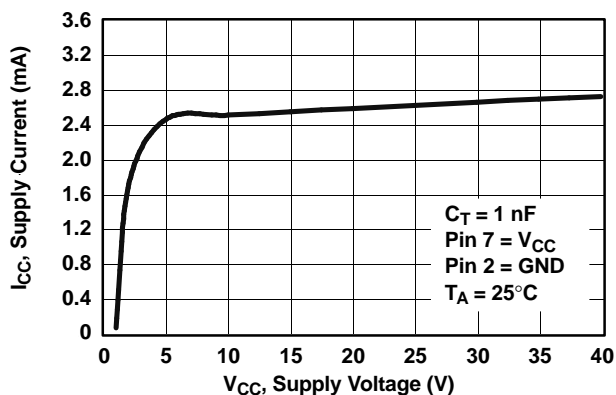
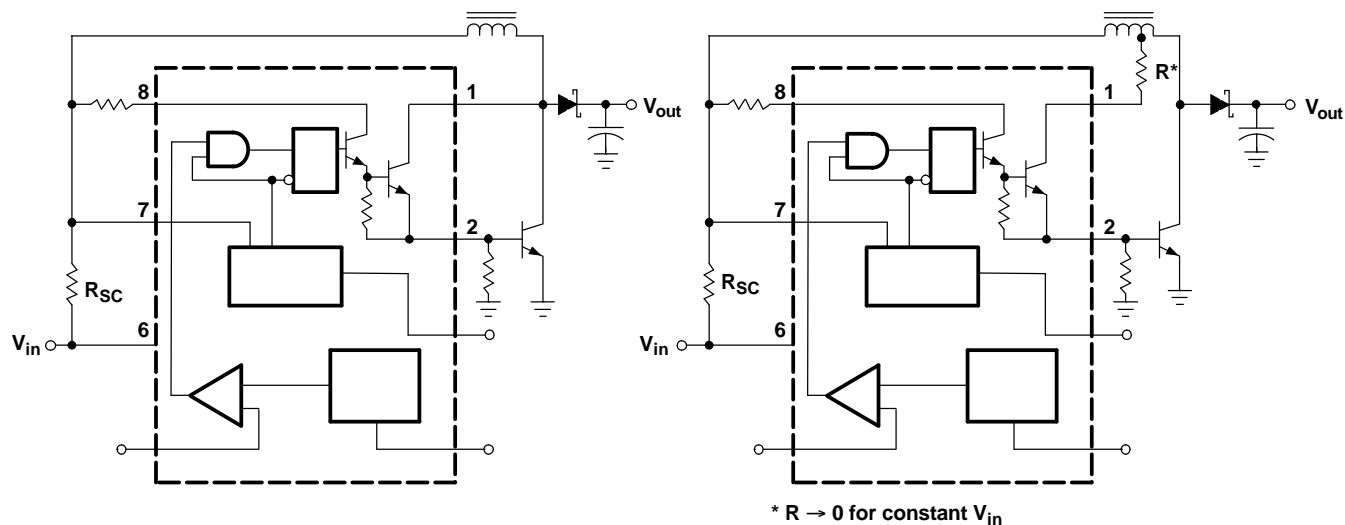


Figure 5. Standby Supply Current vs Supply Voltage



# MC33063A, MC34063A 1.5-A PEAK BOOST/BUCK/INVERTING SWITCHING REGULATORS

SLLS636J–DECEMBER 2004–REVISED OCTOBER 2005



**a) EXTERNAL npn SWITCH**

**b) EXTERNAL pnp SATURATED SWITCH (see Note A)**

- A. If the output switch is driven into hard saturation (non-Darlington configuration) at low switch currents ( $\leq 300$  mA) and high driver currents ( $\geq 30$  mA), it may take up to  $2 \mu\text{s}$  to come out of saturation. This condition will shorten the off time at frequencies  $\geq 30$  kHz and is magnified at high temperatures. This condition does not occur with a Darlington configuration because the output switch cannot saturate. If a non-Darlington configuration is used, the output drive configuration in Figure 7b is recommended.

**Figure 7. External Current-Boost Connections for  $I_C$  Peak Greater Than 1.5 A**

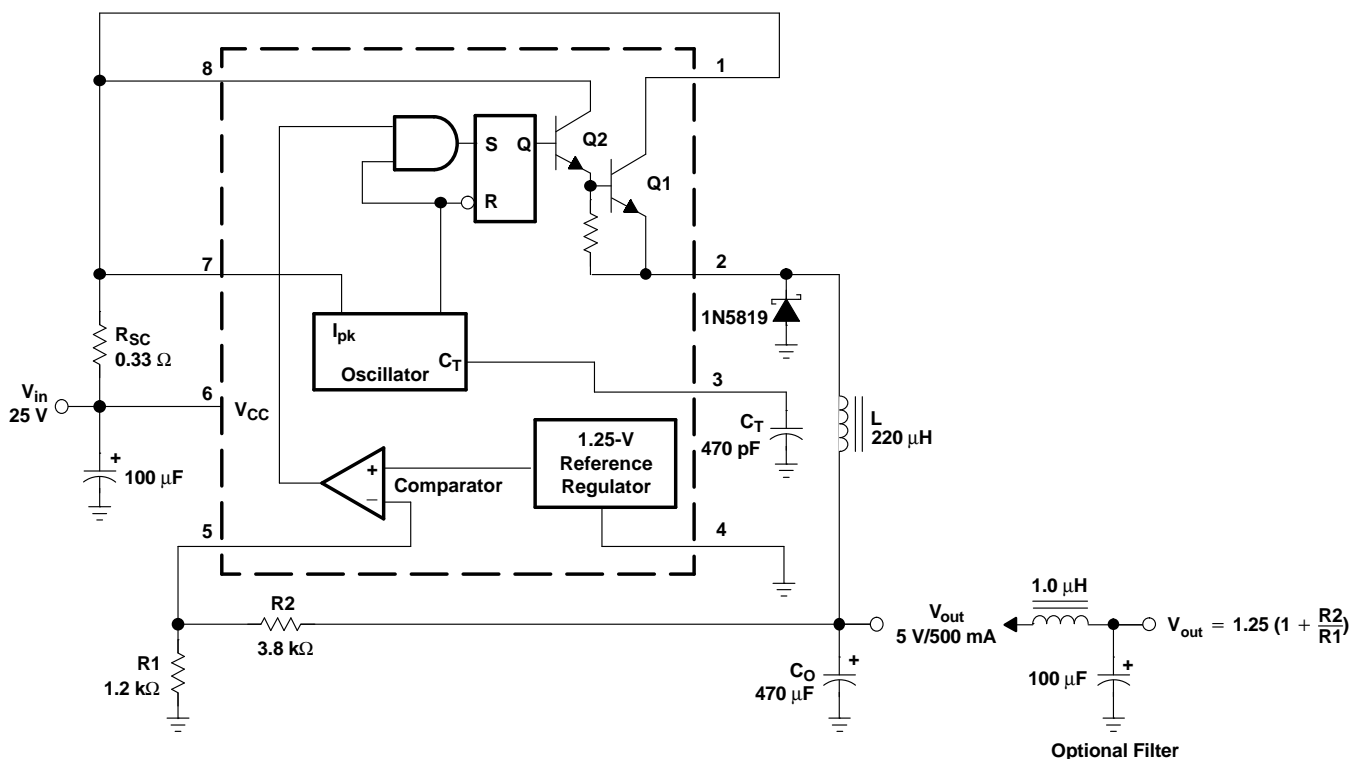


Figure 8. Step-Down Converter

TEST	CONDITIONS	RESULTS
Line regulation	$V_{IN} = 15\text{ V to }25\text{ V}$ , $I_O = 500\text{ mA}$	$12\text{ mV} \pm 0.12\%$
Load regulation	$V_{IN} = 25\text{ V}$ , $I_O = 50\text{ mA to }500\text{ mA}$	$3\text{ mV} \pm 0.03\%$
Output ripple	$V_{IN} = 25\text{ V}$ , $I_O = 500\text{ mA}$	$120\text{ mV}_{PP}$
Short-circuit current	$V_{IN} = 25\text{ V}$ , $R_L = 0.1\ \Omega$	$1.1\text{ A}$
Efficiency	$V_{IN} = 25\text{ V}$ , $I_O = 500\text{ mA}$	$83.7\%$
Output ripple with optional filter	$V_{IN} = 25\text{ V}$ , $I_O = 500\text{ mA}$	$40\text{ mV}_{PP}$

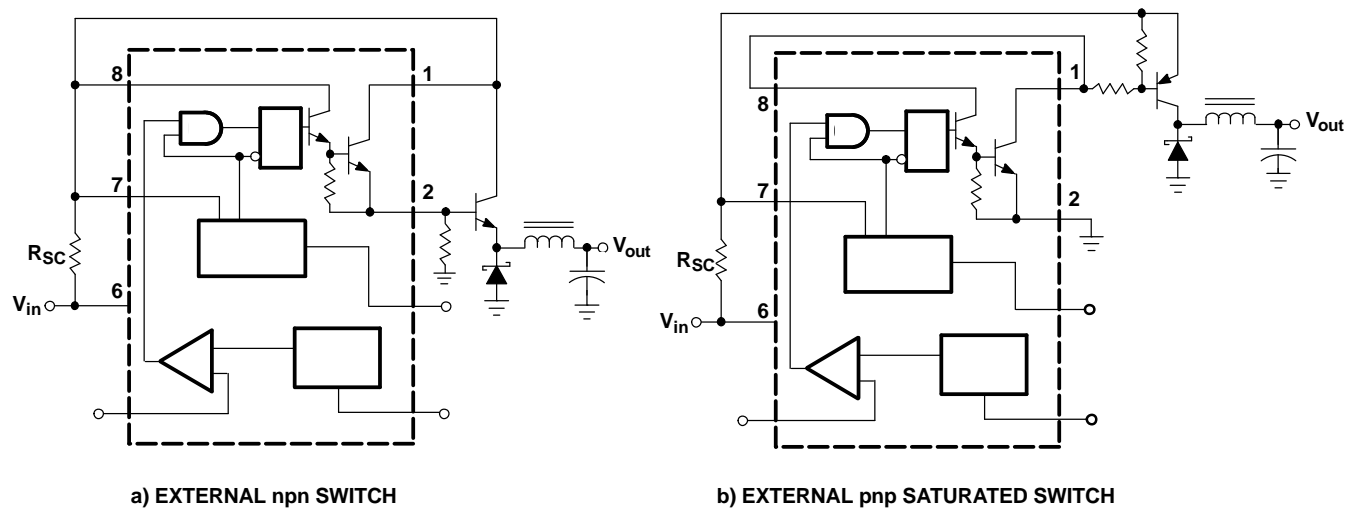
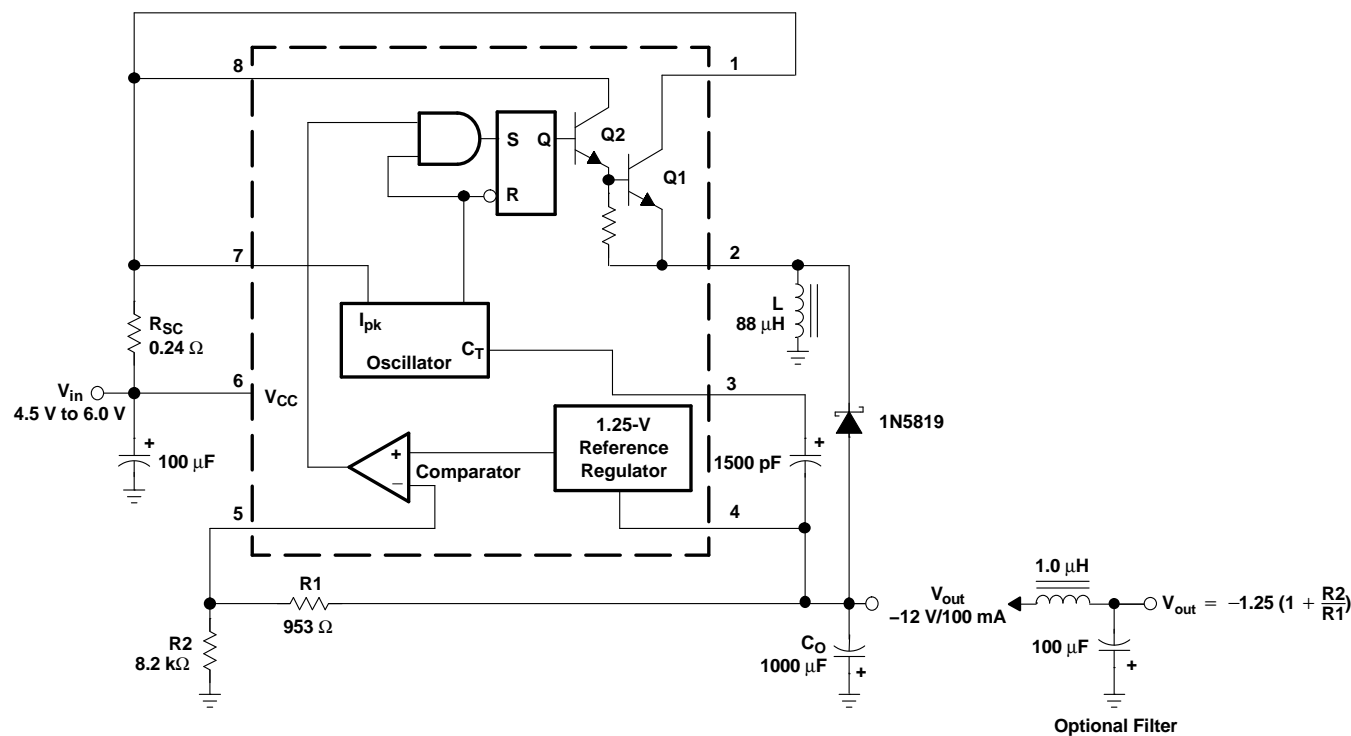


Figure 9. External Current-Boost Connections for  $I_C$  Peak Greater Than 1.5 A

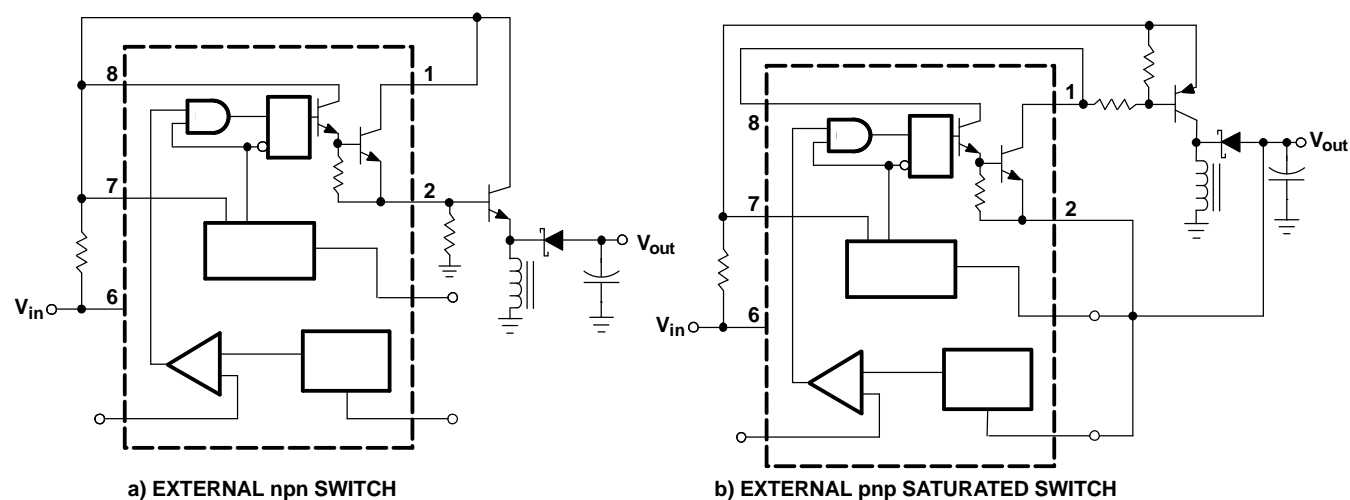
# MC33063A, MC34063A 1.5-A PEAK BOOST/BUCK/INVERTING SWITCHING REGULATORS

SLLS636J–DECEMBER 2004–REVISED OCTOBER 2005



**Figure 10. Voltage-Inverting Converter**

TEST	CONDITIONS	RESULTS
Line regulation	$V_{IN} = 4.5 \text{ V to } 6 \text{ V}$ , $I_O = 100 \text{ mA}$	$3 \text{ mV} \pm 0.12\%$
Load regulation	$V_{IN} = 5 \text{ V}$ , $I_O = 10 \text{ mA to } 100 \text{ mA}$	$0.022 \text{ V} \pm 0.09\%$
Output ripple	$V_{IN} = 5 \text{ V}$ , $I_O = 100 \text{ mA}$	$500 \text{ mV}_{PP}$
Short-circuit current	$V_{IN} = 5 \text{ V}$ , $R_L = 0.1 \Omega$	$910 \text{ mA}$
Efficiency	$V_{IN} = 5 \text{ V}$ , $I_O = 100 \text{ mA}$	$62.2\%$
Output ripple with optional filter	$V_{IN} = 5 \text{ V}$ , $I_O = 100 \text{ mA}$	$70 \text{ mV}_{PP}$



**Figure 11. External Current-Boost Connections for  $I_C$  Peak Greater Than 1.5 A**



## APPLICATION INFORMATION

CALCULATION	STEP UP	STEP DOWN	VOLTAGE INVERTING
$t_{on}/t_{off}$	$\frac{V_{out} + V_F - V_{in(min)}}{V_{in(min)} - V_{sat}}$	$\frac{V_{out} + V_F}{V_{in(min)} - V_{sat} - V_{out}}$	$\frac{ V_{out}  + V_F}{V_{in} - V_{sat}}$
$(t_{on} + t_{off})$	$\frac{1}{f}$	$\frac{1}{f}$	$\frac{1}{f}$
$t_{off}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$
$t_{on}$	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$
$C_T$	$4 \times 10^{-5} t_{on}$	$4 \times 10^{-5} t_{on}$	$4 \times 10^{-5} t_{on}$
$I_{pk(switch)}$	$2I_{out(max)} \left( \frac{t_{on}}{t_{off}} + 1 \right)$	$2I_{out(max)}$	$2I_{out(max)} \left( \frac{t_{on}}{t_{off}} + 1 \right)$
$R_{sc}$	$\frac{0.3}{I_{pk(switch)}}$	$\frac{0.3}{I_{pk(switch)}}$	$\frac{0.3}{I_{pk(switch)}}$
$L_{(min)}$	$\left( \frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}} \right) t_{on(max)}$	$\left( \frac{(V_{in(min)} - V_{sat} - V_{out})}{I_{pk(switch)}} \right) t_{on(max)}$	$\left( \frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}} \right) t_{on(max)}$
$C_O$	$9 \frac{I_{out} t_{on}}{V_{ripple(pp)}}$	$\frac{I_{pk(switch)}(t_{on} + t_{off})}{8V_{ripple(pp)}}$	$9 \frac{I_{out} t_{on}}{V_{ripple(pp)}}$
$V_{out}$	$1.25(1 + \frac{R2}{R1})$ See Figure 6	$1.25(1 + \frac{R2}{R1})$ See Figure 8	$-1.25(1 + \frac{R2}{R1})$ See Figure 10

$V_{sat}$  = Saturation voltage of the output switch

$V_F$  = Forward voltage drop of the chosen output rectifier

The following power-supply parameters are set by the user:

$V_{in}$  = Nominal input voltage

$V_{out}$  = Desired output voltage

$I_{out}$  = Desired output current

$f_{min}$  = Minimum desired output switching frequency at the selected values of  $V_{in}$  and  $I_{out}$

$V_{ripple}$  = Desired peak-to-peak output ripple voltage. The ripple voltage directly affects the line and load regulation and, thus, must be considered. In practice, the actual capacitor value should be larger than the calculated value, to account for the capacitor's equivalent series resistance and board layout.

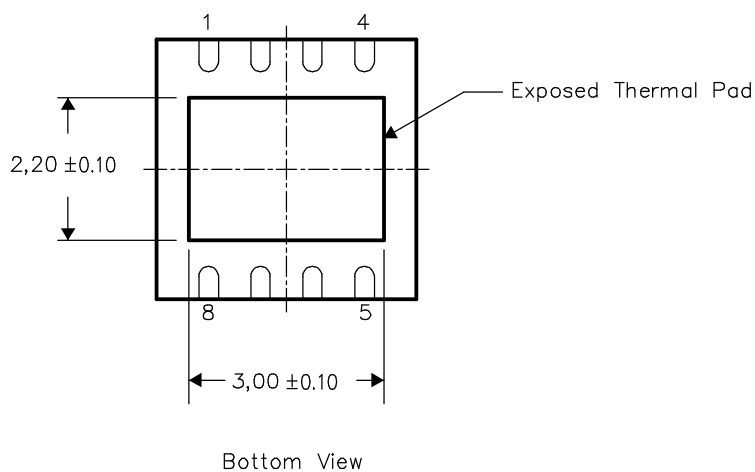
**THERMAL PAD MECHANICAL DATA**  
**DRJ (S-PDSO-N8)**

THERMAL INFORMATION

This package incorporates an exposed thermal pad that is designed to be attached directly to an external heatsink. The thermal pad must be soldered directly to the printed circuit board (PCB), the PCB can be used as a heatsink. In addition, through the use of thermal vias, the thermal pad can be attached directly to a ground plane or special heatsink structure designed into the PCB. This design optimizes the heat transfer from the integrated circuit (IC).

For information on the Quad Flatpack No-Lead (QFN) package and its advantages, refer to Application Report, Quad Flatpack No-Lead Logic Packages, Texas Instruments Literature No. SCBA017. This document is available at [www.ti.com](http://www.ti.com).

The exposed thermal pad dimensions for this package are shown in the following illustration.



NOTE: All linear dimensions are in millimeters

Exposed Thermal Pad Dimensions

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
MC33063AD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC33063ADE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC33063ADR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC33063ADRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC33063AP	ACTIVE	PDIP	P	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
MC33063APE4	ACTIVE	PDIP	P	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
MC34063AD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC34063ADE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC34063ADR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC34063ADRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC34063AP	ACTIVE	PDIP	P	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
MC34063APE4	ACTIVE	PDIP	P	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

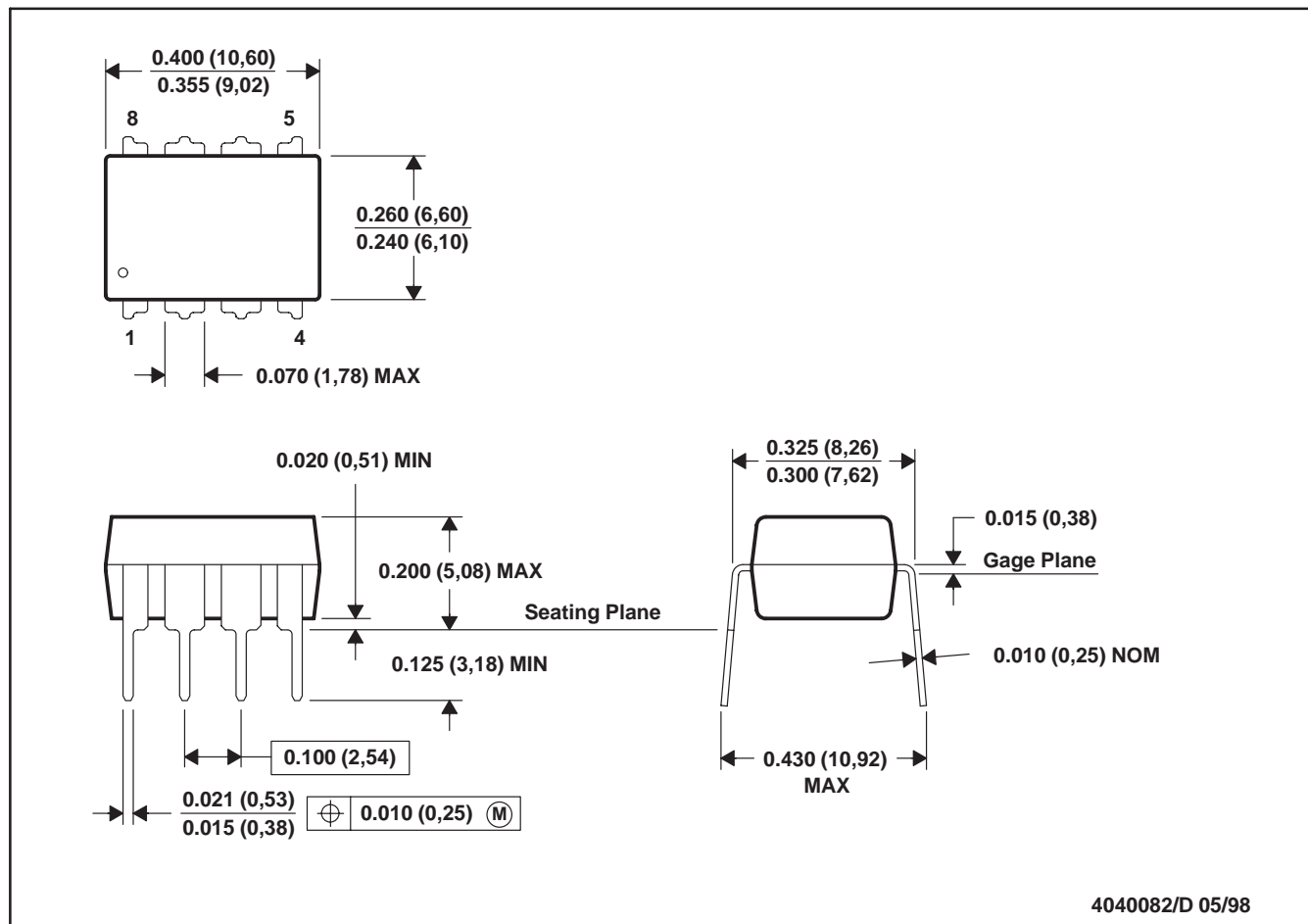
<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

## P (R-PDIP-T8)

## PLASTIC DUAL-IN-LINE

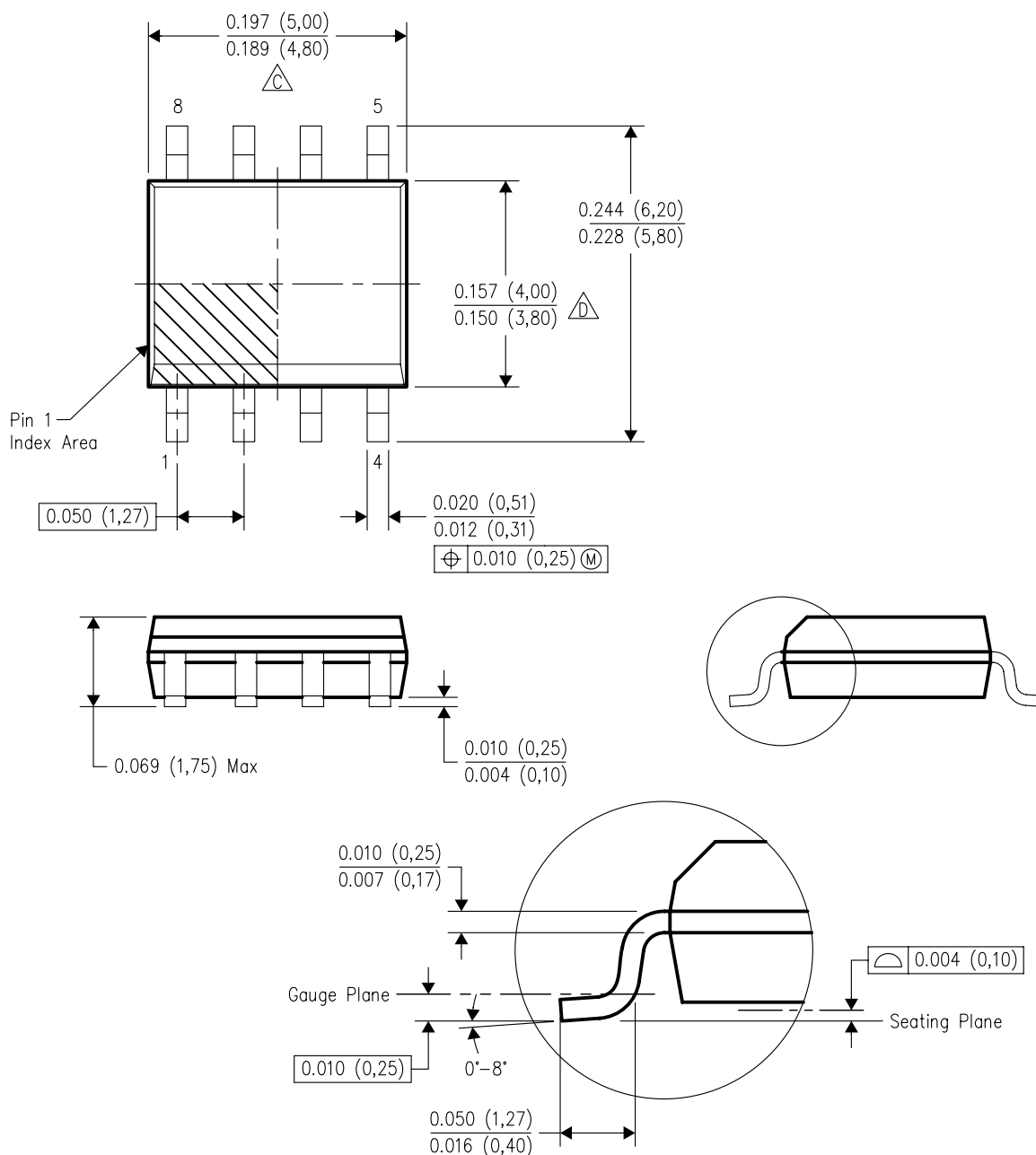


- NOTES: A. All linear dimensions are in inches (millimeters).  
 B. This drawing is subject to change without notice.  
 C. Falls within JEDEC MS-001

For the latest package information, go to [http://www.ti.com/sc/docs/package/pkg\\_info.htm](http://www.ti.com/sc/docs/package/pkg_info.htm)

## D (R-PDSO-G8)

# PLASTIC SMALL-OUTLINE PACKAGE



4040047-2/H 11/2006

NOTES:

- A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.  
**C** Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.  
**D** Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.  
E. Reference JEDEC MS-012 variation AA.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

<b>Products</b>		<b>Applications</b>	
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>	Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>	Automotive	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>	Broadband	<a href="http://www.ti.com/broadband">www.ti.com/broadband</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>	Digital Control	<a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>	Military	<a href="http://www.ti.com/military">www.ti.com/military</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>	Optical Networking	<a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>	Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Low Power Wireless	<a href="http://www.ti.com/lpw">www.ti.com/lpw</a>	Telephony	<a href="http://www.ti.com/telephony">www.ti.com/telephony</a>
		Video & Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>
		Wireless	<a href="http://www.ti.com/wireless">www.ti.com/wireless</a>

Mailing Address: Texas Instruments  
Post Office Box 655303 Dallas, Texas 75265

Copyright © 2006, Texas Instruments Incorporated